

FIG. 1

100

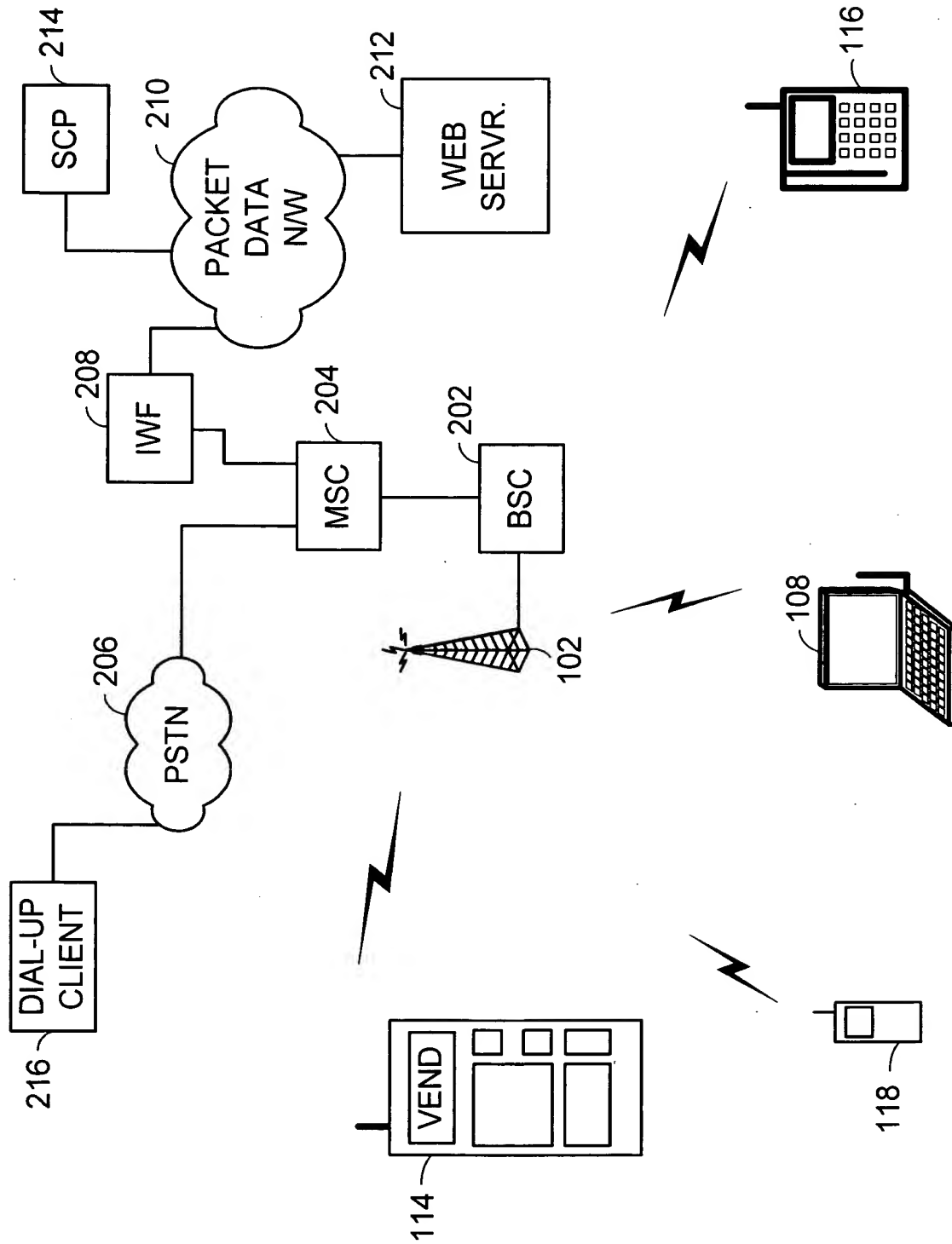


FIG. 2

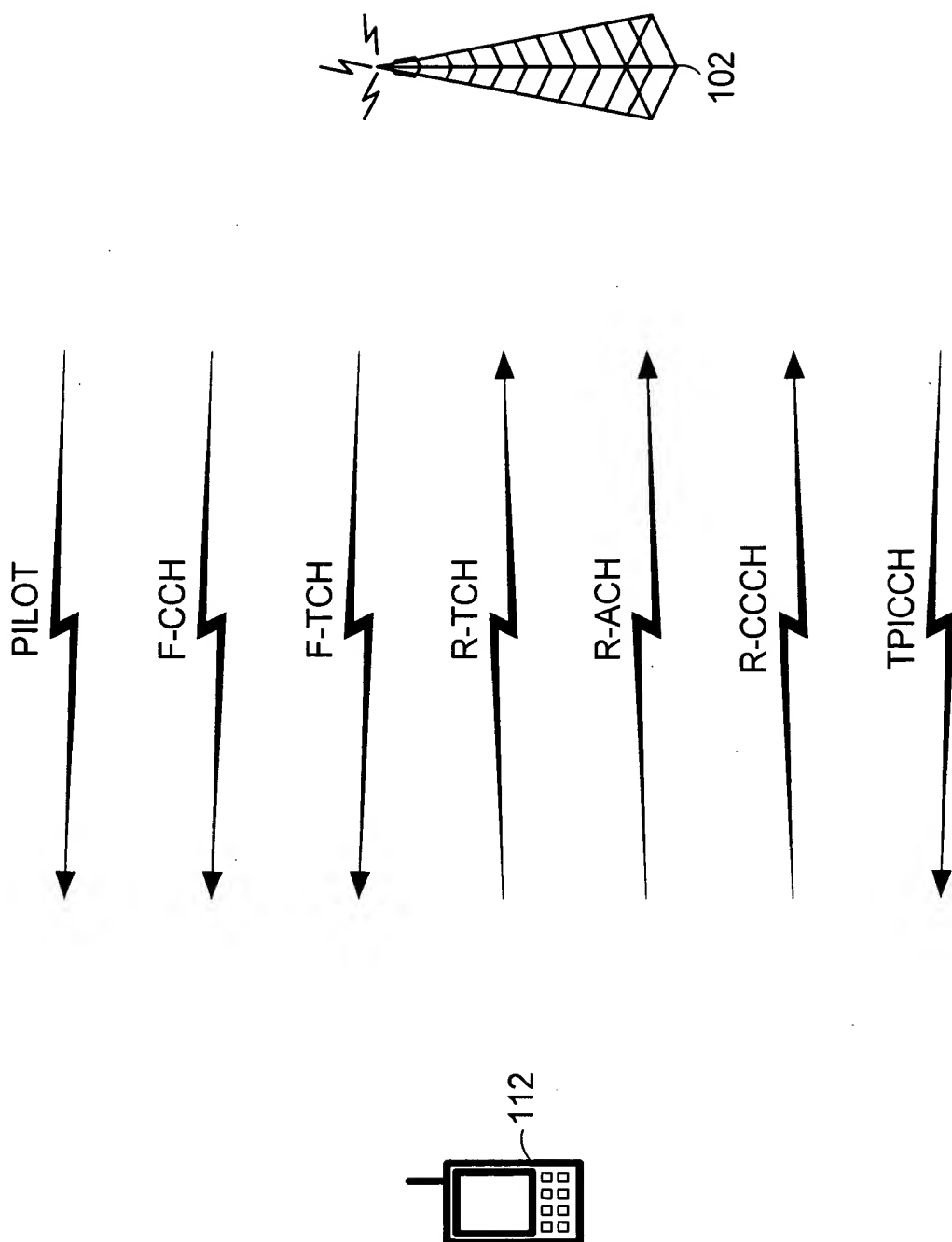
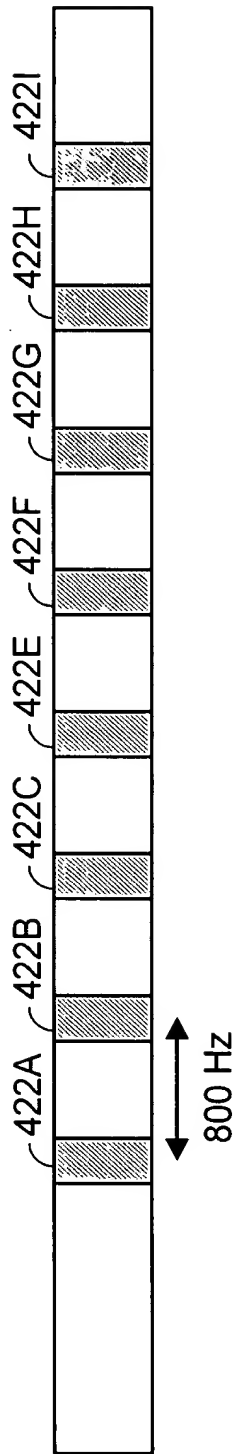
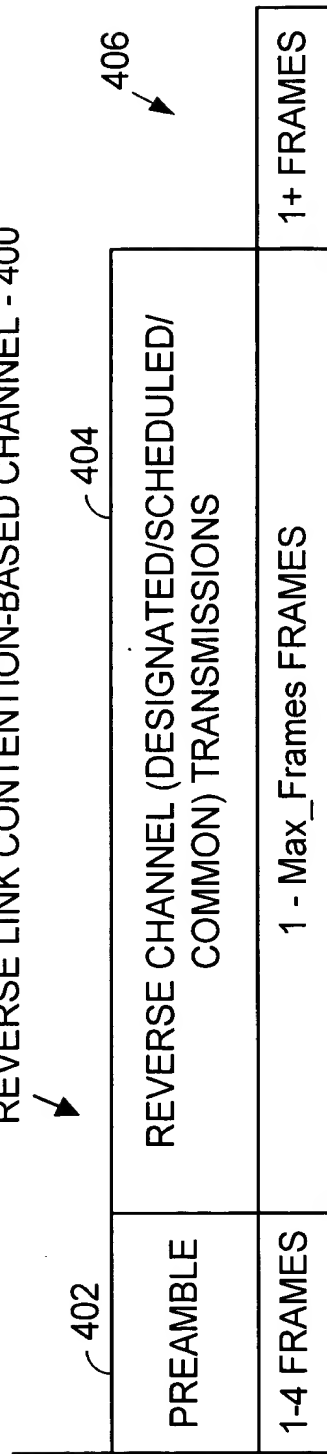


FIG. 3

TRANSMIT POWER AND ISMA CONTROL CHANNEL - 420



REVERSE LINK CONTENTION-BASED CHANNEL - 400



OFFSET 0 - 440



FIG. 4

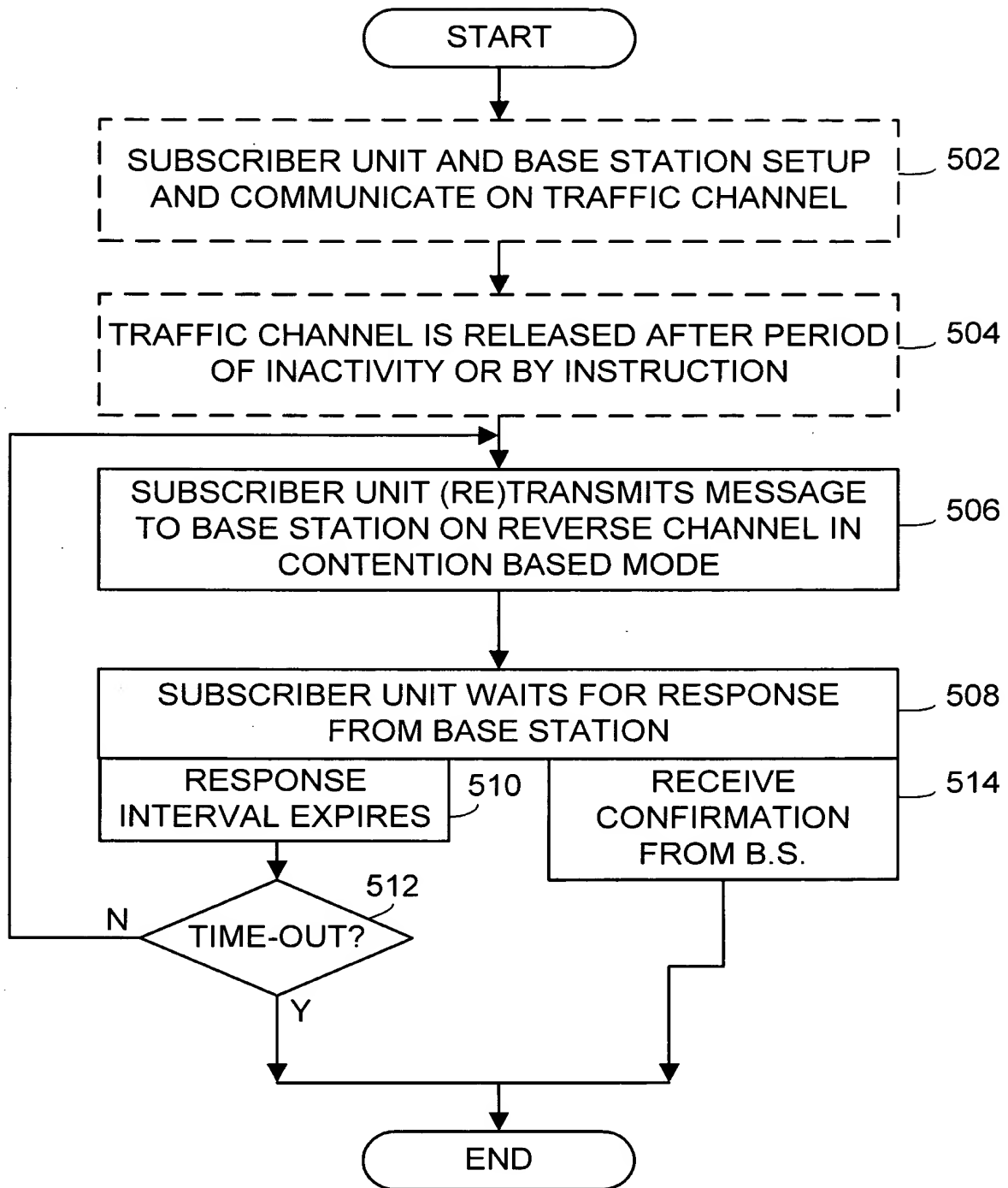
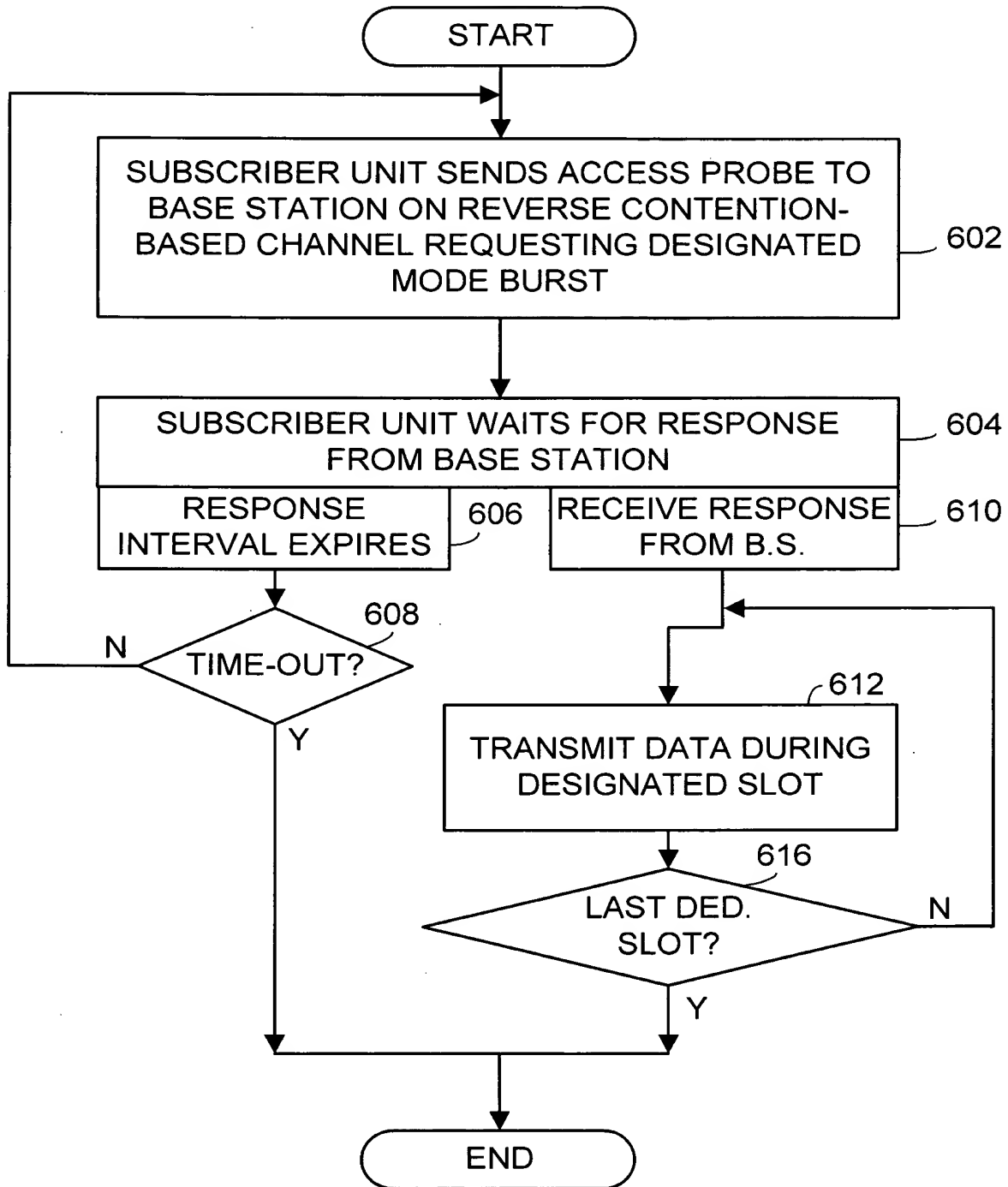


FIG. 5

**FIG. 6**

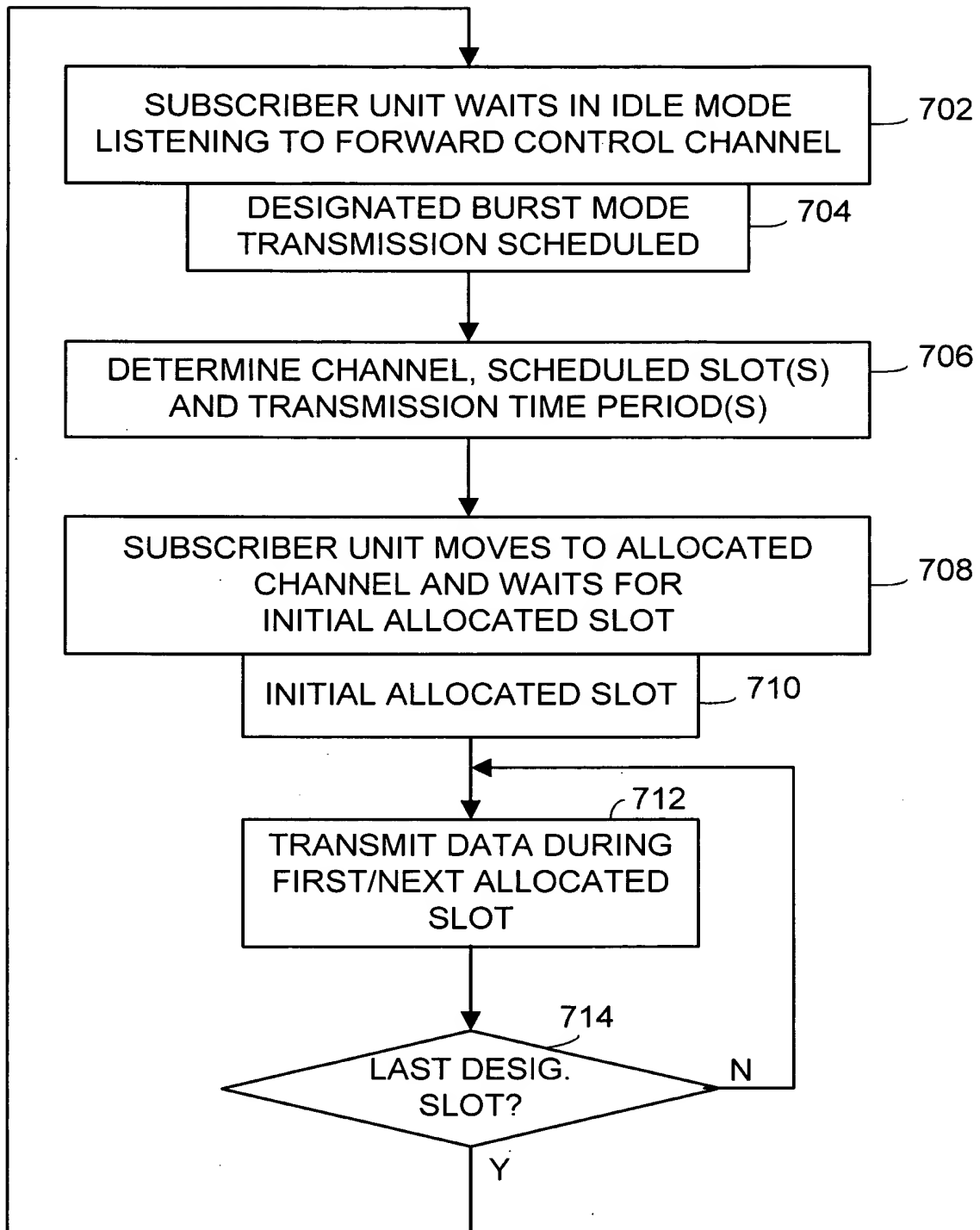
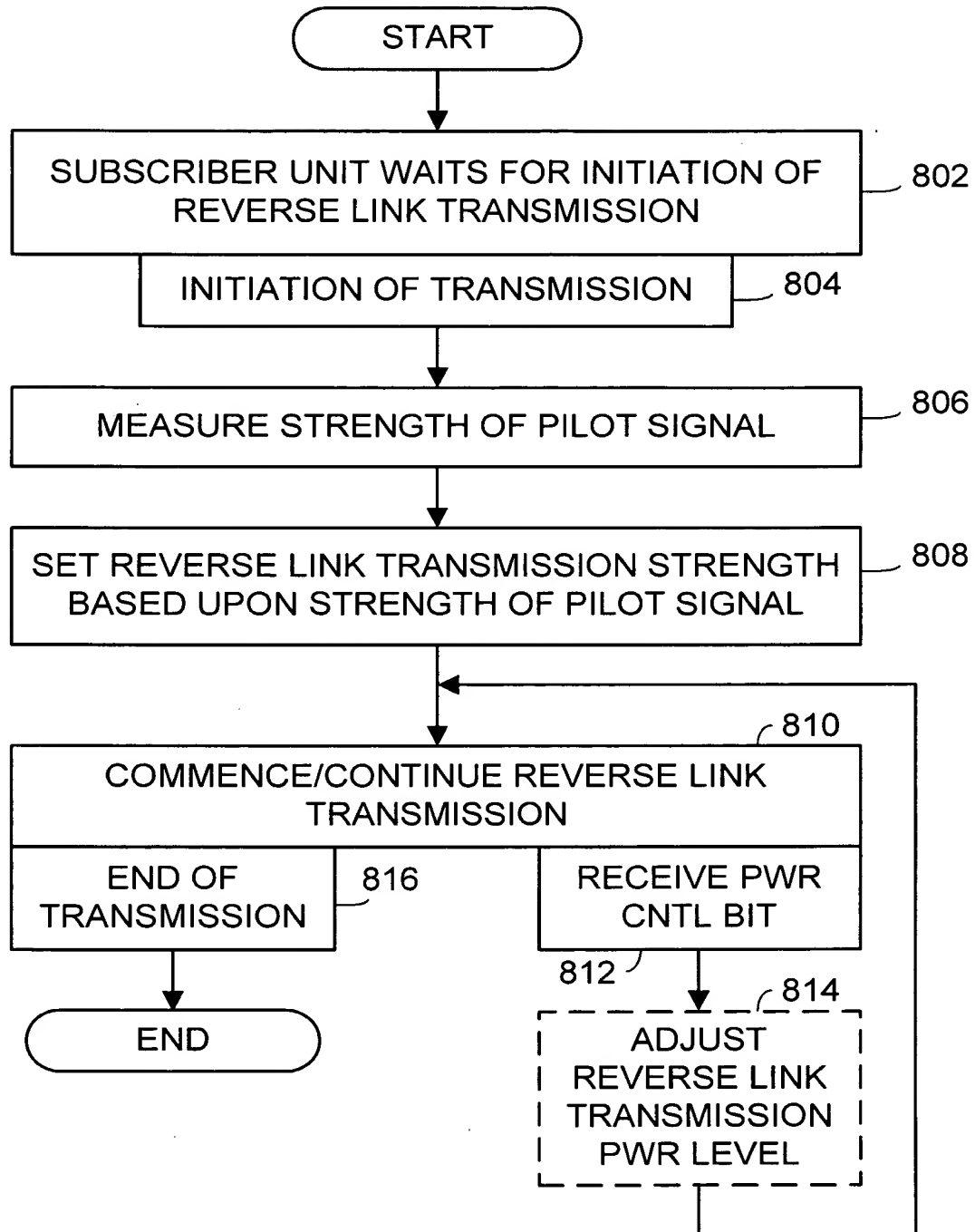


FIG. 7

**FIG. 8**

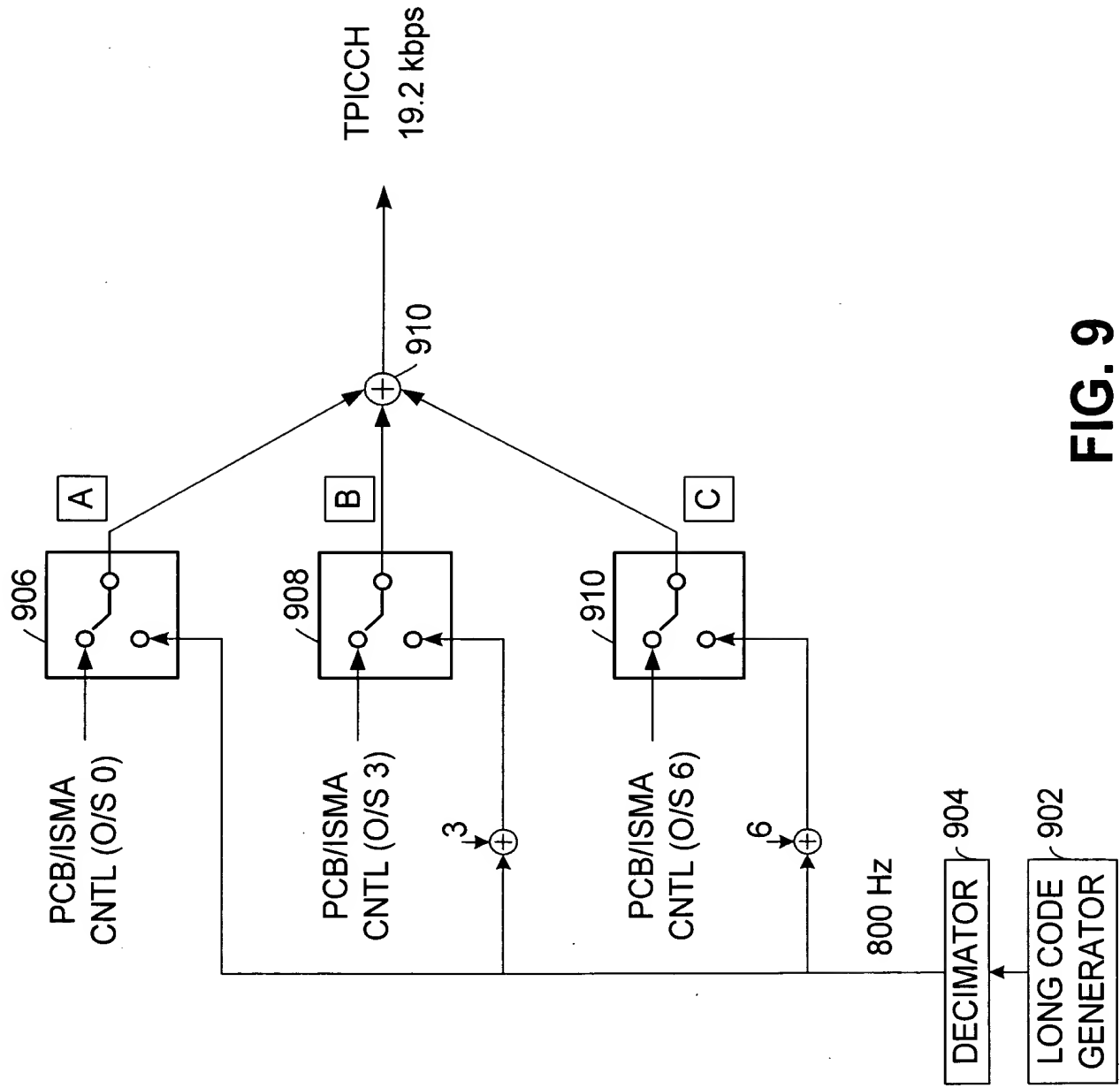


FIG. 9

EXAMPLE: POWER AND/OR ISMA CONTROL BIT STARTING TIMES

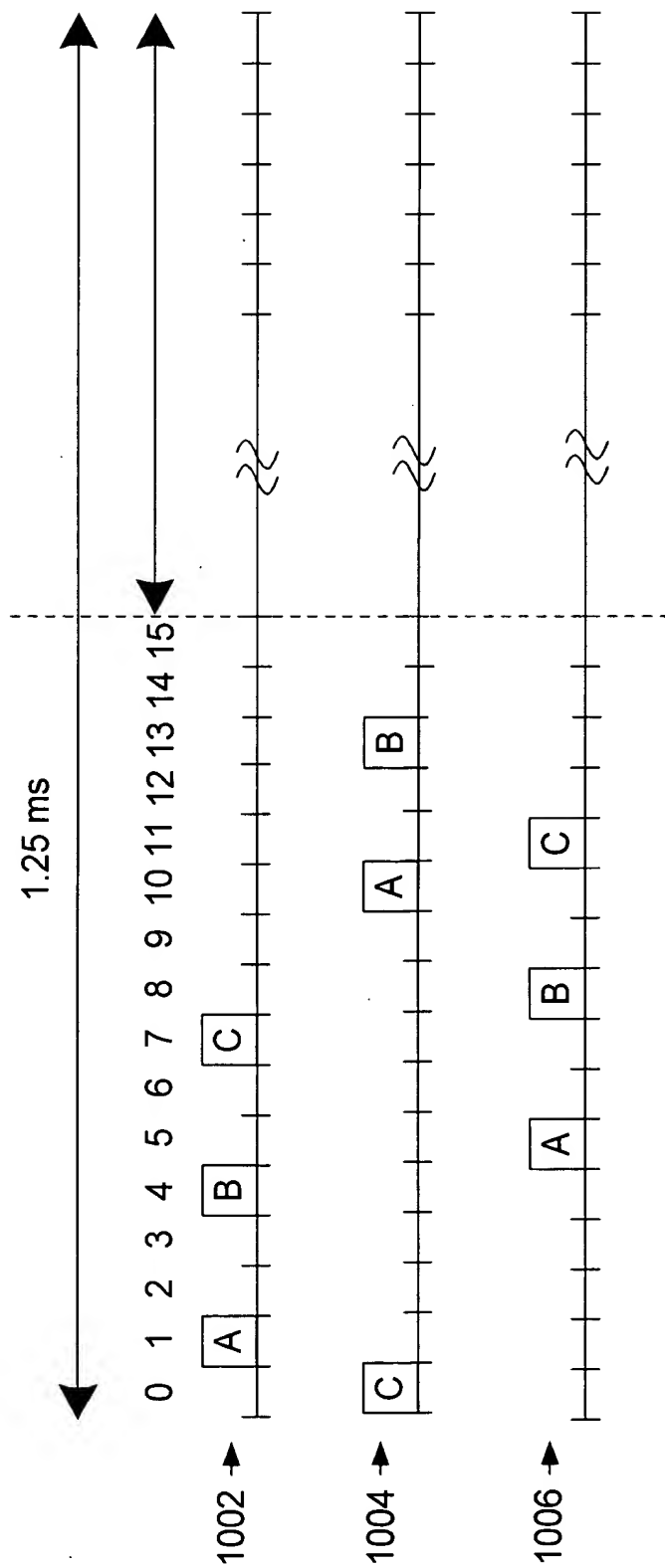


FIG. 10

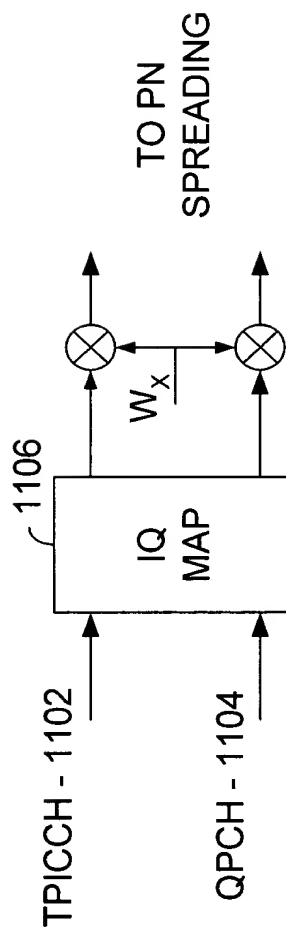


FIG. 11

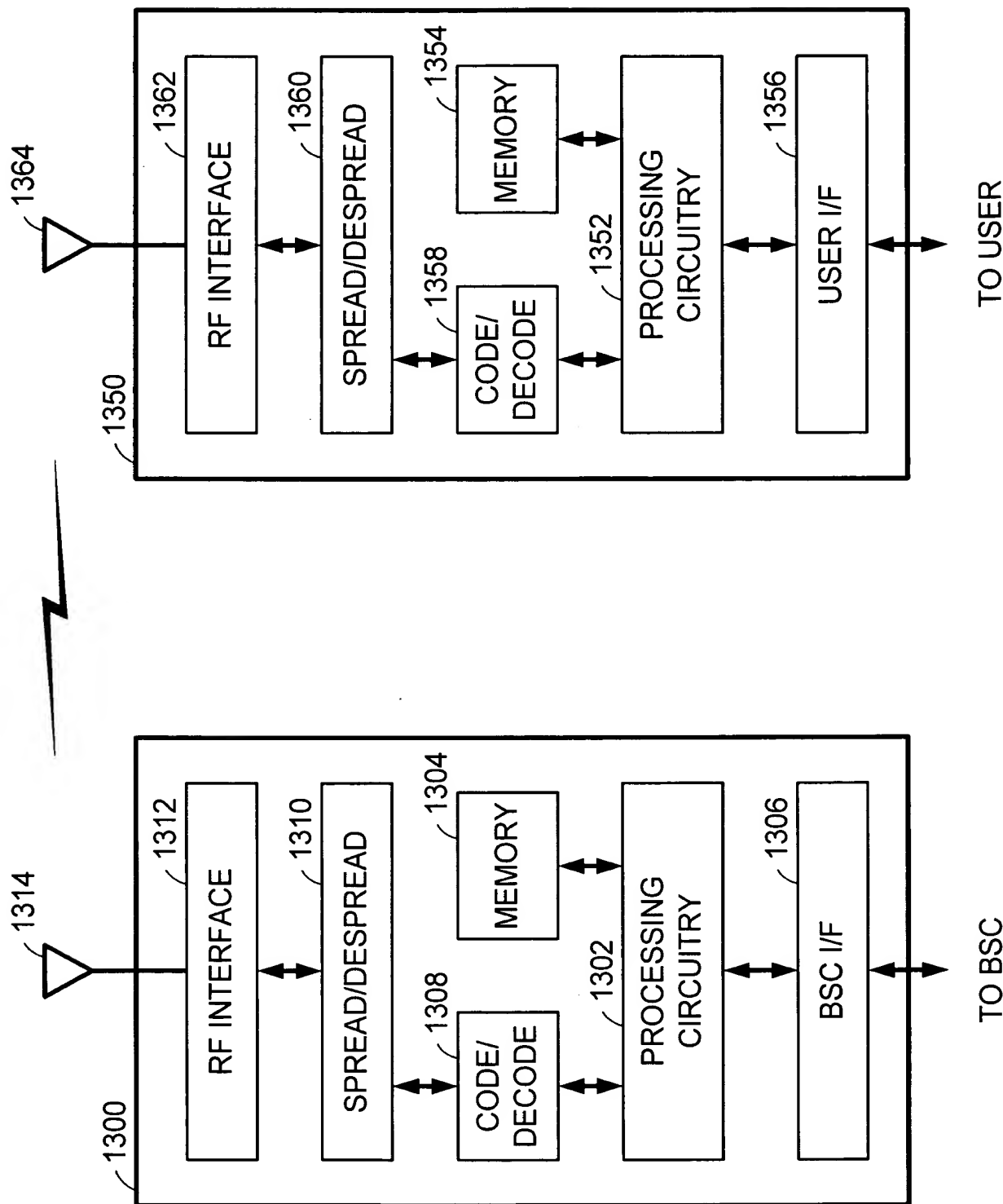
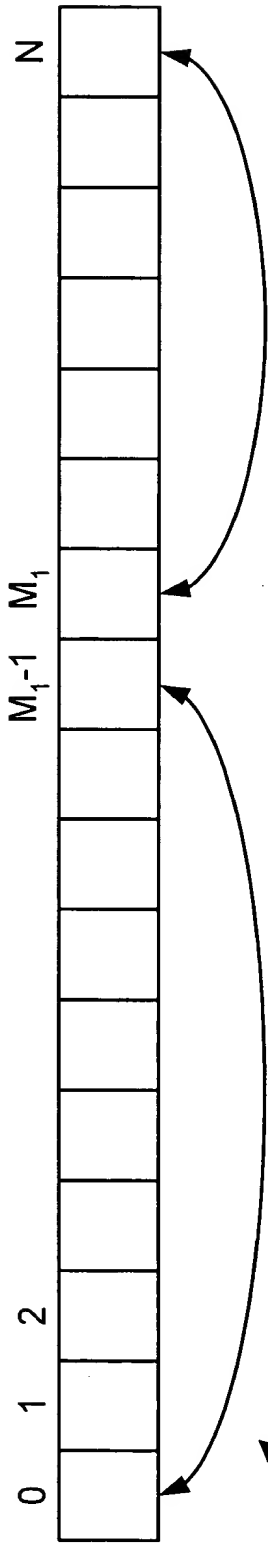


FIG. 12

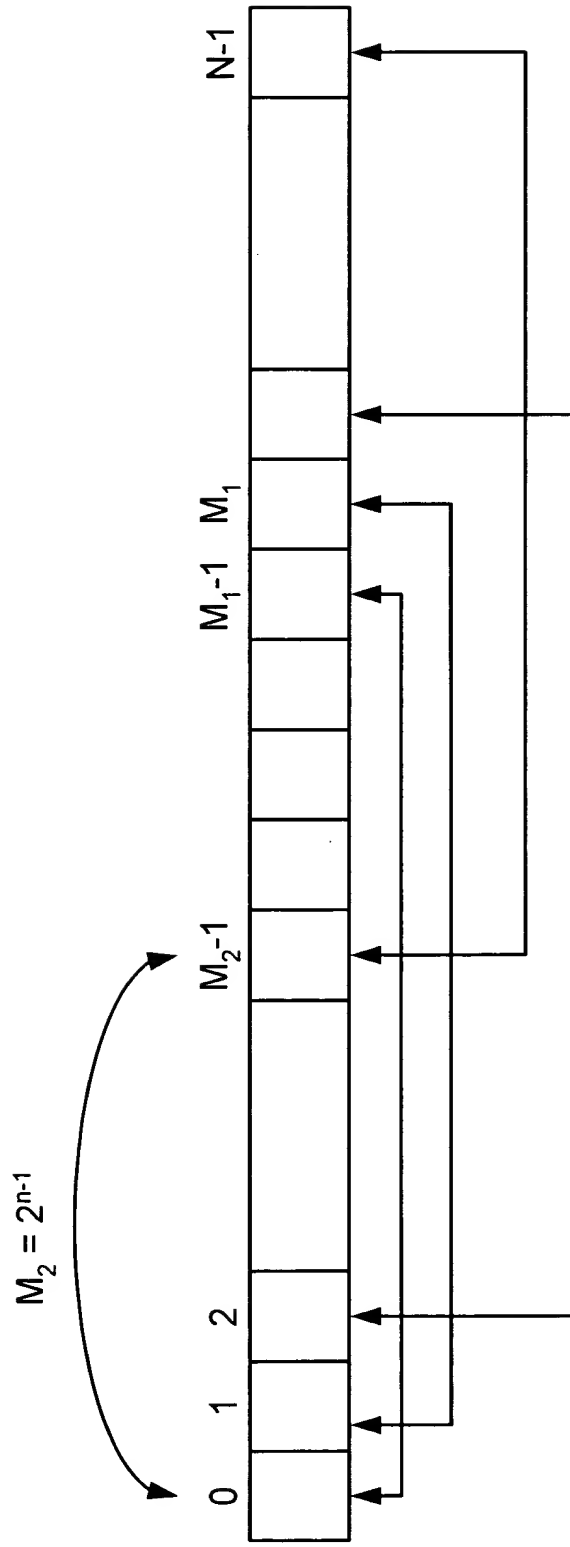


1400

1402 - GROUP I: $M_1 = 2^n$

1404 - GROUP II

FIG. 13



1500

FIG. 14